

**150V N-Ch Power MOSFET**
**Feature**

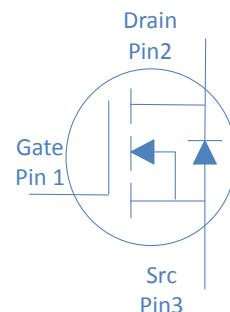
- ◇ Optimized for high speed smooth switching, Logic level
- ◇ Enhanced Body diode dv/dt capability
- ◇ Enhanced Avalanche Ruggedness
- ◇ 100% UIS Tested, 100% Rg Tested

$V_{DS}$	150	V
$R_{DS(on),typ}$	$V_{GS}=10V$	16.0 mΩ
$R_{DS(on),typ}$	$V_{GS}=4.5V$	19 mΩ
$I_D$ (Silicon Limited)	69	A

**Application**

- ◇ DC-DC Conversion
- ◇ Hard Switching and High Speed Circuit
- ◇ Power Tools
- ◇ UPS
- ◇ SSR

TO-220



Part Number	Package	Marking
HGP190N15SL	TO-220	P190N15SL

**Absolute Maximum Ratings at  $T_j=25^\circ\text{C}$  (unless otherwise specified)**

Parameter	Symbol	Conditions	Value	Unit
Continuous Drain Current (Silicon Limited)	$I_D$	$T_C=25^\circ\text{C}$	69	A
		$T_C=100^\circ\text{C}$	49	
Drain to Source Voltage	$V_{DS}$	-	150	V
Gate to Source Voltage	$V_{GS}$	-	$\pm 20$	V
Pulsed Drain Current	$I_{DM}$	-	150	A
Avalanche Energy, Single Pulse	$E_{AS}$	$L=0.3\text{mH}, T_C=25^\circ\text{C}$	184	mJ
Power Dissipation	$P_D$	$T_C=25^\circ\text{C}$	214	W
Operating and Storage Temperature	$T_J, T_{stg}$	-	-55 to 175	°C

**Absolute Maximum Ratings**

Parameter	Symbol	Max	Unit
Thermal Resistance Junction-Case	$R_{eJC}$	0.7	°C/W
Thermal Resistance Junction-Ambient	$R_{eJA}$	50	°C/W

**Electrical Characteristics at  $T_j=25^\circ\text{C}$  (unless otherwise specified)**
**Static Characteristics**

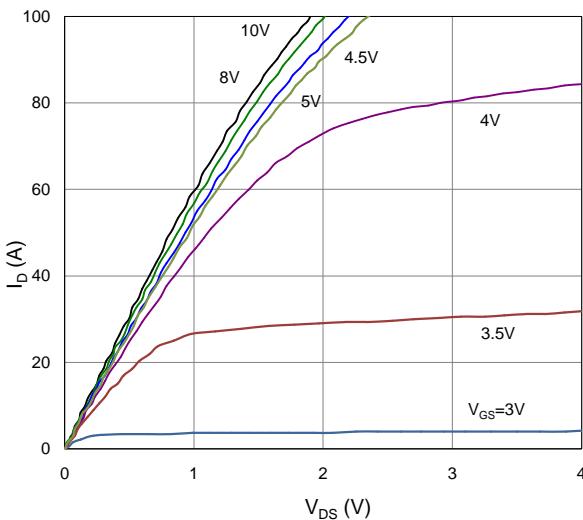
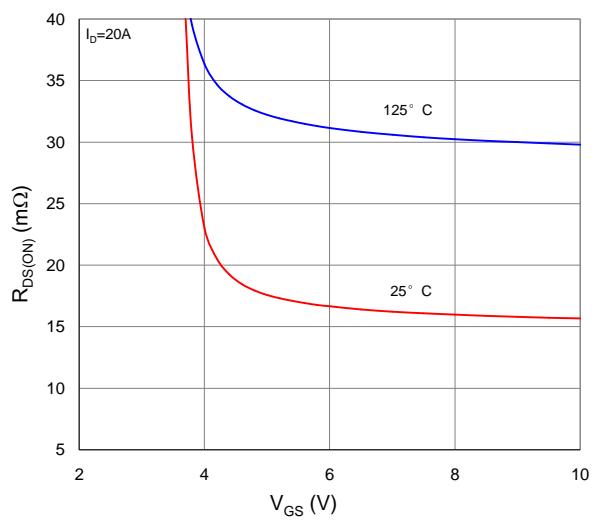
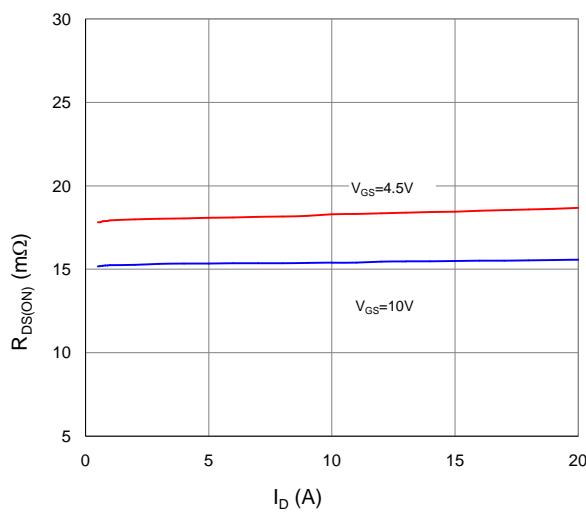
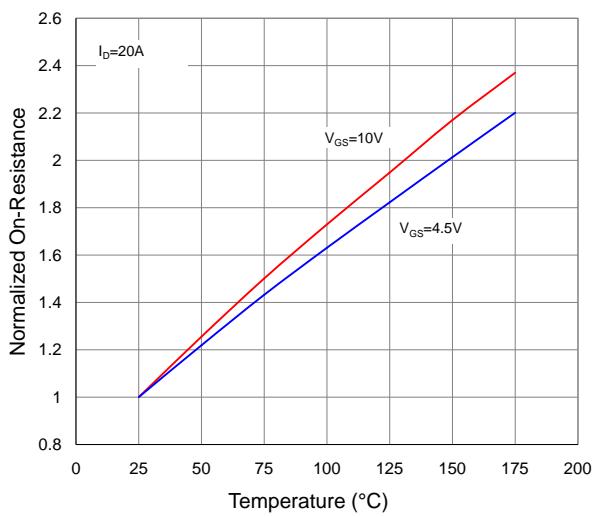
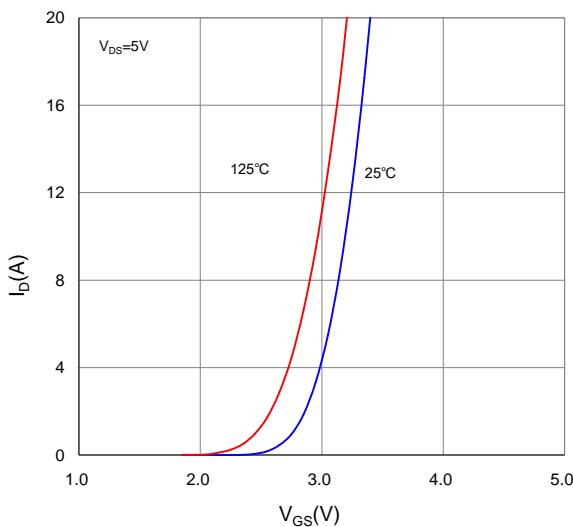
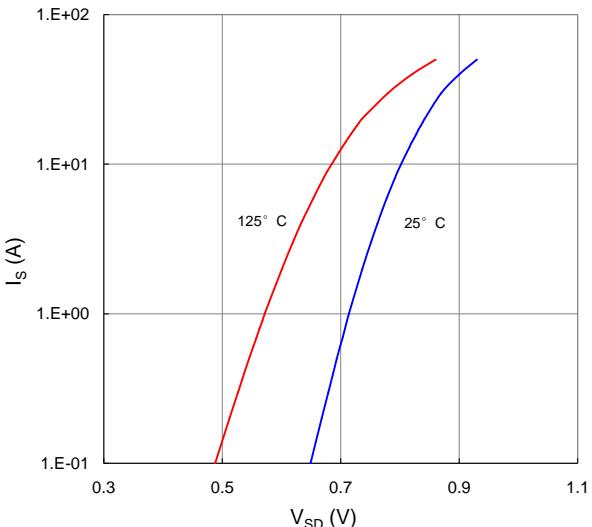
Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	150	-	-	V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}, I_D=250\mu\text{A}$	1	1.7	3	
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=150\text{V}, T_j=25^\circ\text{C}$	-	-	1	$\mu\text{A}$
		$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=150\text{V}, T_j=100^\circ\text{C}$	-	-	100	
Gate to Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	nA
Drain to Source on Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=10\text{V}, I_D=20\text{A}$	-	16	19	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_D=20\text{A}$	-	19	25	$\text{m}\Omega$
Transconductance	$g_{\text{fs}}$	$V_{\text{DS}}=5\text{V}, I_D=20\text{A}$	-	53	-	S
Gate Resistance	$R_G$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}} \text{ Open}, f=1\text{MHz}$	-	3.5	-	$\Omega$

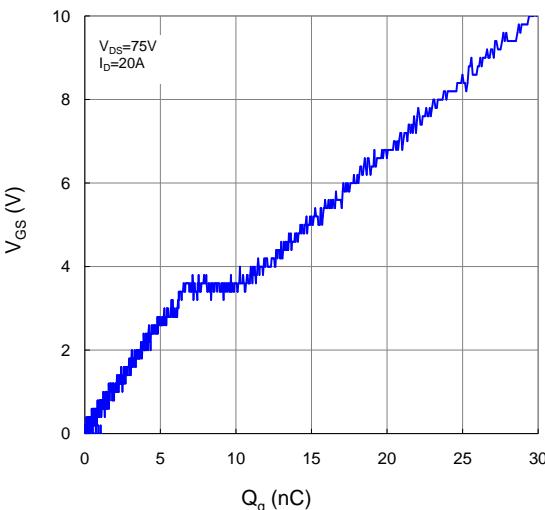
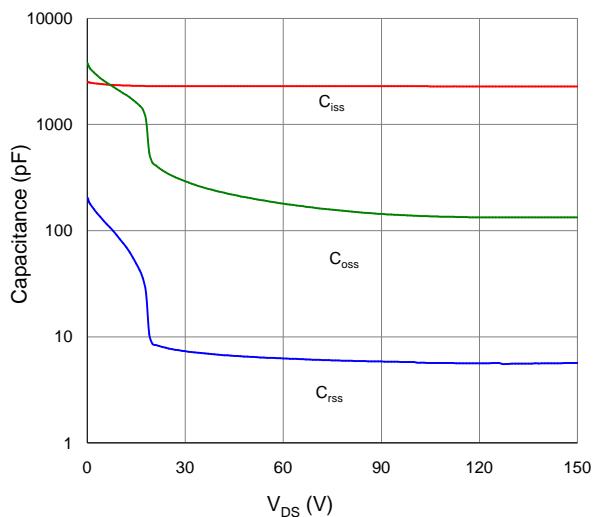
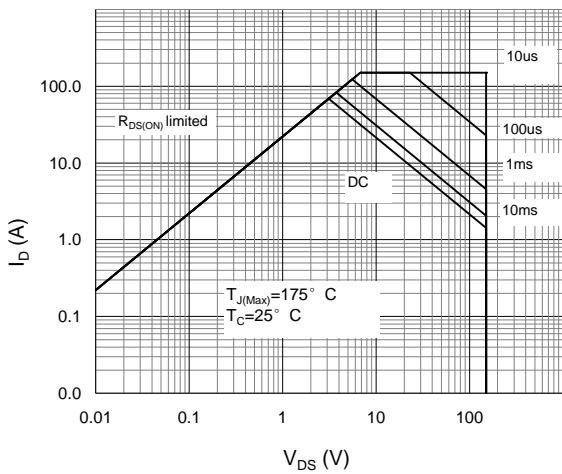
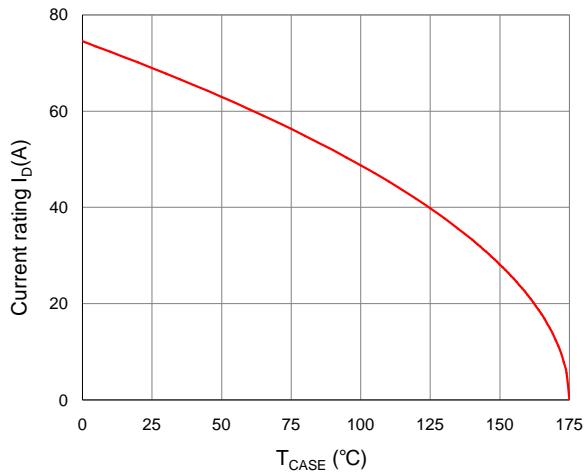
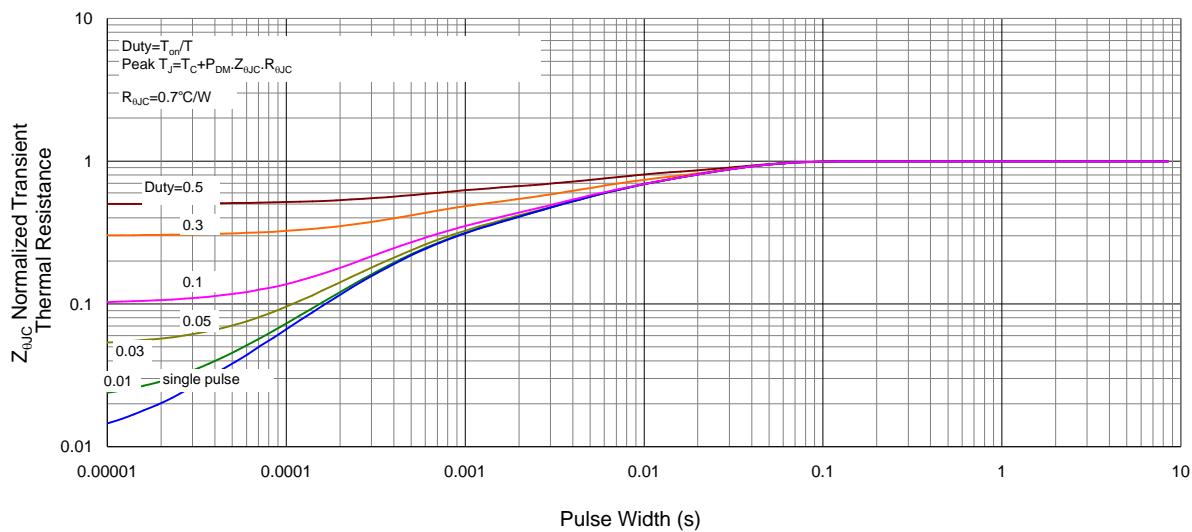
**Dynamic Characteristics**

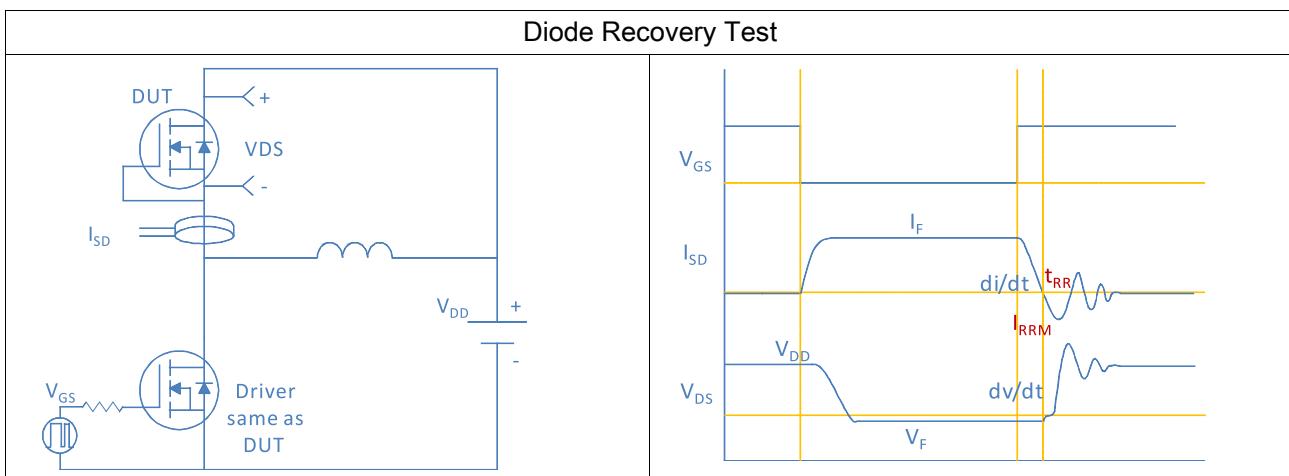
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=75\text{V}, f=1\text{MHz}$	-	2300	-	pF
Output Capacitance	$C_{\text{oss}}$		-	158	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	6	-	
Total Gate Charge	$Q_g(10\text{V})$	$V_{\text{DD}}=75\text{V}, I_D=20\text{A}, V_{\text{GS}}=10\text{V}$	-	30	-	nC
Total Gate Charge	$Q_g(4.5\text{V})$		-	13.5	-	
Gate to Source Charge	$Q_{\text{gs}}$		-	6.5	-	
Gate to Drain (Miller) Charge	$Q_{\text{gd}}$		-	3.5	-	
Turn on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=75\text{V}, I_D=20\text{A}, V_{\text{GS}}=10\text{V}, R_G=10\Omega$	-	13	-	ns
Rise time	$t_r$		-	4	-	
Turn off Delay Time	$t_{\text{d}(\text{off})}$		-	26	-	
Fall Time	$t_f$		-	6	-	

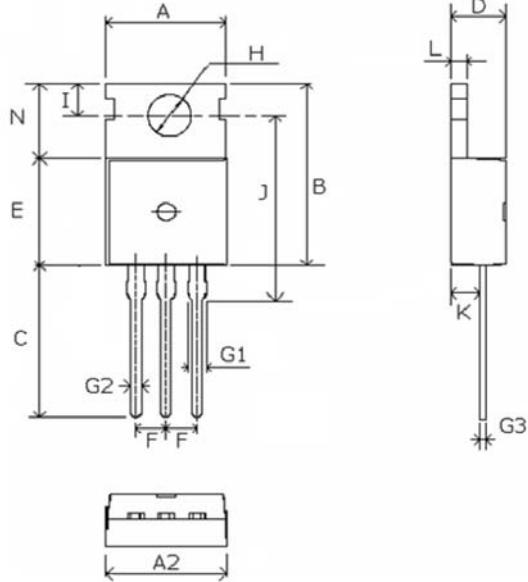
**Reverse Diode Characteristics**

Diode Forward Voltage	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_F=20\text{A}$	-	0.9	1.2	V
Reverse Recovery Time	$t_{\text{rr}}$	$V_R=75\text{V}, I_F=20\text{A}, dI_F/dt=100\text{A}/\mu\text{s}$	-	90	-	ns
Reverse Recovery Charge	$Q_{\text{rr}}$		-	270	-	nC

**Fig 1. Typical Output Characteristics**

**Figure 2. On-Resistance vs. Gate-Source Voltage**

**Figure 3. On-Resistance vs. Drain Current and Gate Voltage**

**Figure 4. Normalized On-Resistance vs. Junction Temperature**

**Figure 5. Typical Transfer Characteristics**

**Figure 6. Typical Source-Drain Diode Forward Voltage**


**Figure 7. Typical Gate-Charge vs. Gate-to-Source Voltage**

**Figure 8. Typical Capacitance vs. Drain-to-Source Voltage**

**Figure 9. Maximum Safe Operating Area**

**Figure 10. Maximum Drain Current vs. Case Temperature**

**Figure 11. Normalized Maximum Transient Thermal Impedance, Junction-to-Case**




**Package Outline**
**TO-220, 3 leads**

**Dimentions in mm unless otherwise specified**

Symbol	Min	Nom	Max
A	9.66	9.97	10.28
A2	9.80	10.00	10.20
B	15.60	15.70	15.80
C	12.70	13.48	14.27
D	4.30	4.50	4.70
E	9.00	9.20	9.40
F		2.54	
G1	1.32	1.52	1.72
G2	0.70	0.82	0.95
G3	0.45	0.52	0.60
H	3.50	3.60	3.70
I	2.70	2.80	2.90
J	15.70	15.97	16.25
K	2.20	2.40	2.60
L	1.15	1.27	1.40
N	6.40	6.60	6.80