

**100V N-Ch Power MOSFET**
**Feature**

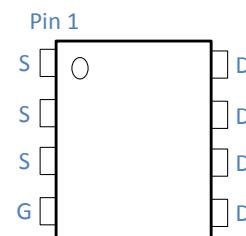
- ◇ High Speed Power Switching, Logic Level
- ◇ Enhanced Body diode dv/dt capability
- ◇ Enhanced Avalanche Ruggedness
- ◇ 100% UIS Tested, 100% Rg Tested
- ◇ Lead Free, Halogen Free

$V_{DS}$	100	V
$R_{DS(on),typ}$	$V_{GS}=10V$	22.0 mΩ
$R_{DS(on),typ}$	$V_{GS}=4.5V$	26 mΩ
$I_D$ (Silicon Limited)	21	A

**Application**

- ◇ Synchronous Rectification in SMPS
- ◇ Hard Switching and High Speed Circuit
- ◇ DC/DC in Telecoms and Industrial

Part Number	Package	Marking
HGM290N10SL	DFN 3.3*3.3	GM290N10L

**DFN3.3x3.3**

**Absolute Maximum Ratings at  $T_j=25^\circ C$  (unless otherwise specified)**

Parameter	Symbol	Conditions	Value	Unit
Continuous Drain Current (Silicon Limited)	$I_D$	$T_C=25^\circ C$	21	A
		$T_C=100^\circ C$	13	
Drain to Source Voltage	$V_{DS}$	-	100	V
Gate to Source Voltage	$V_{GS}$	-	$\pm 20$	V
Pulsed Drain Current	$I_{DM}$	-	60	A
Avalanche Energy, Single Pulse	$E_{AS}$	$L=0.4mH, T_C=25^\circ C$	20	mJ
Power Dissipation	$P_D$	$T_C=25^\circ C$	25	W
Operating and Storage Temperature	$T_J, T_{stg}$	-	-55 to 150	°C

**Absolute Maximum Ratings**

Parameter	Symbol	Max	Unit
Thermal Resistance Junction-Ambient	$R_{\theta JA}$	55	°C/W
Thermal Resistance Junction-Case	$R_{\theta JC}$	5	°C/W

**Electrical Characteristics at  $T_j=25^\circ\text{C}$  (unless otherwise specified)**
**Static Characteristics**

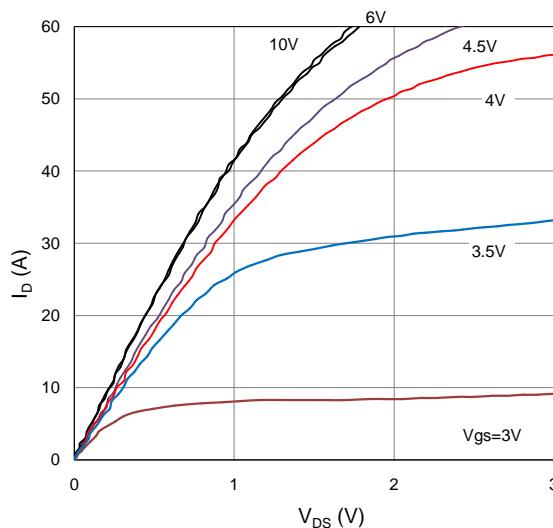
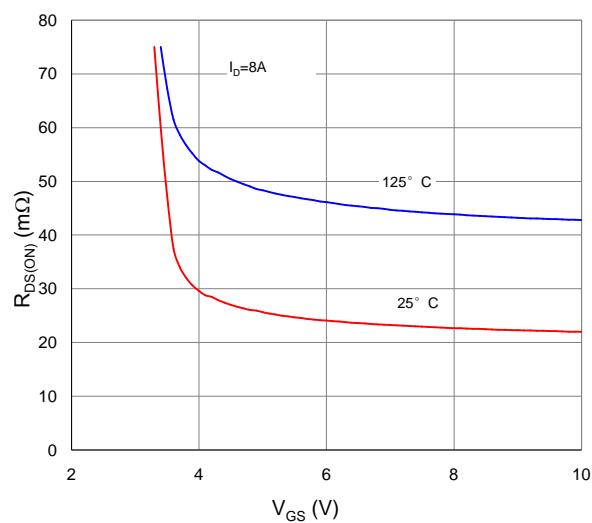
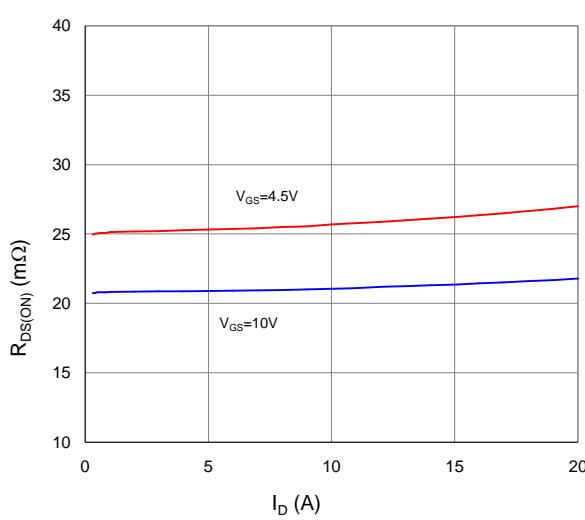
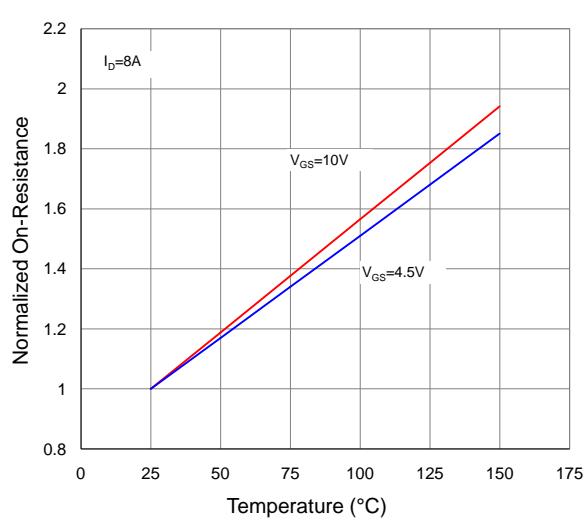
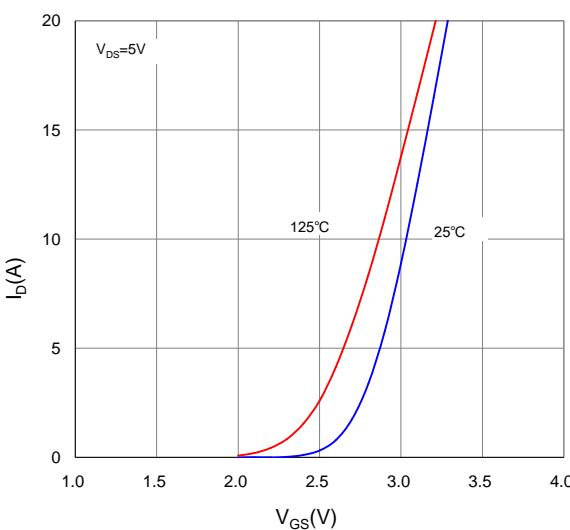
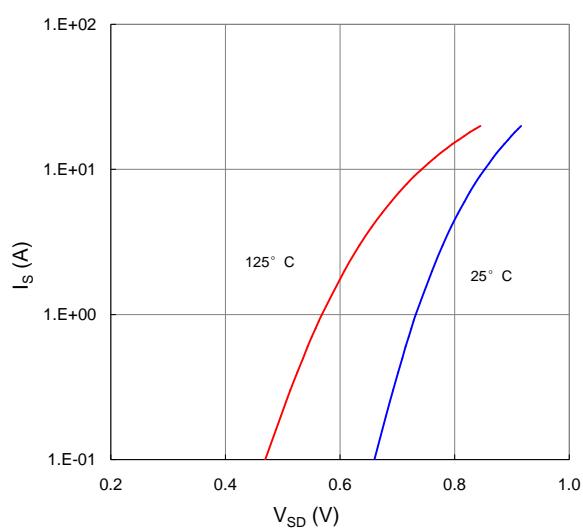
Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	100	-	-	V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}, I_D=250\mu\text{A}$	1.4	2	2.4	
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=100\text{V}, T_j=25^\circ\text{C}$	-	-	1	$\mu\text{A}$
		$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=100\text{V}, T_j=100^\circ\text{C}$	-	-	100	
Gate to Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	nA
Drain to Source on Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=10\text{V}, I_D=8\text{A}$	-	22	29	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_D=6\text{A}$	-	26	36	
Transconductance	$g_{\text{fs}}$	$V_{\text{DS}}=5\text{V}, I_D=8\text{A}$	-	4.7	-	S
Gate Resistance	$R_G$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}} \text{ Open}, f=1\text{MHz}$	-	1.5	-	$\Omega$

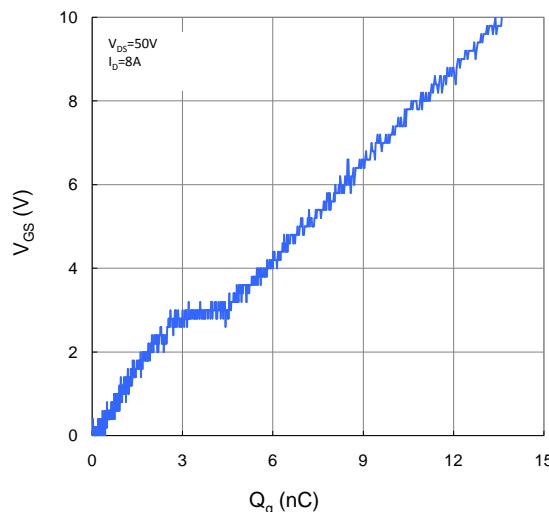
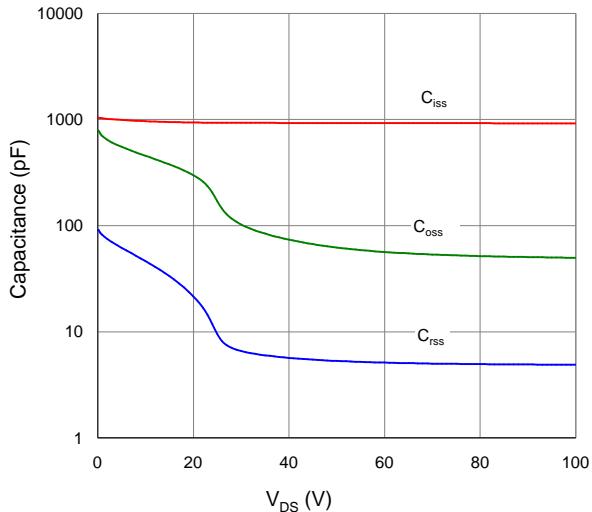
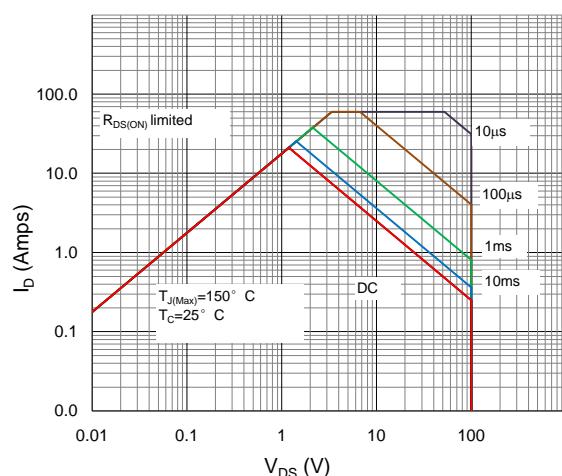
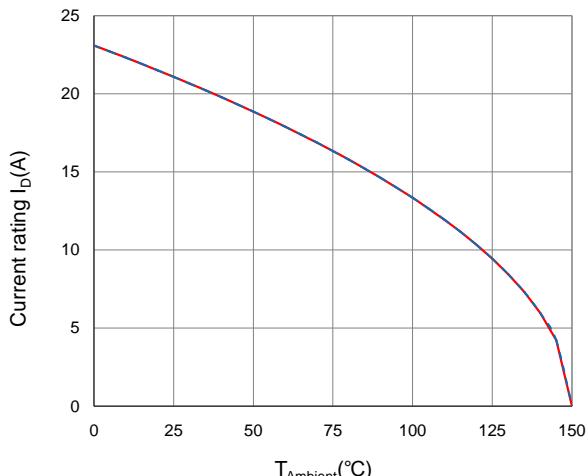
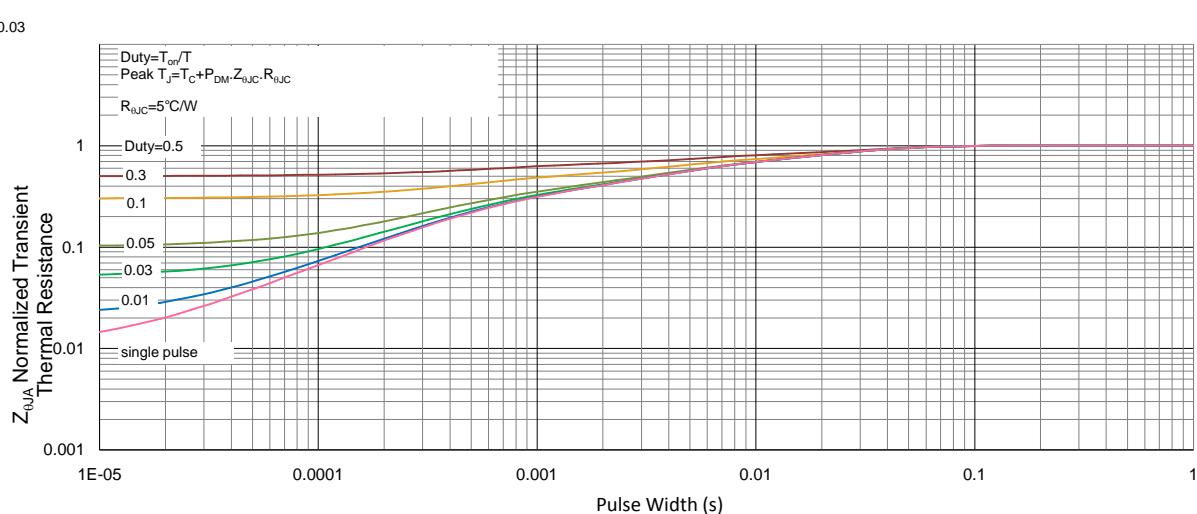
**Dynamic Characteristics**

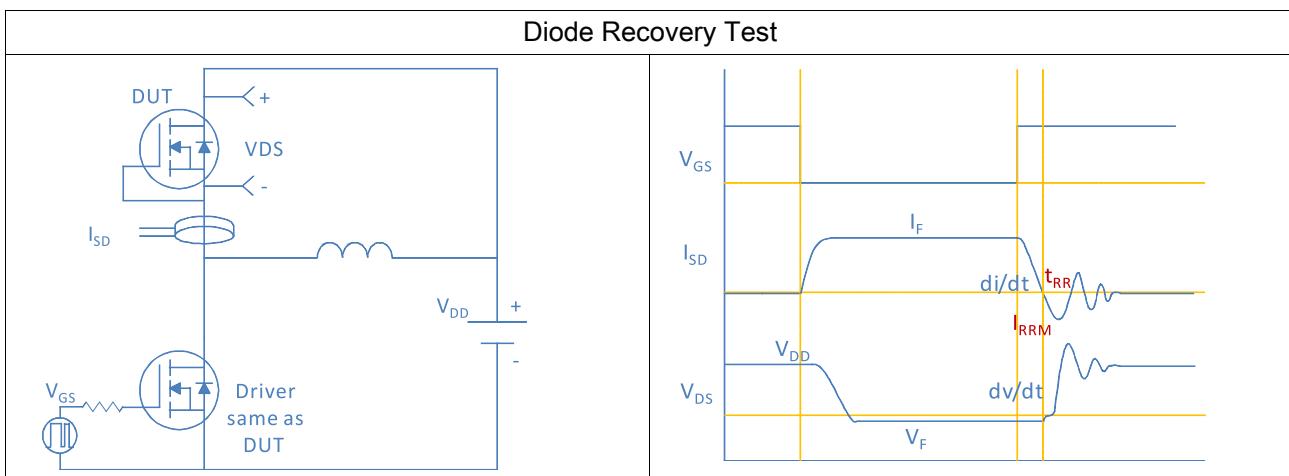
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=50\text{V}, f=1\text{MHz}$	-	930	-	pF
Output Capacitance	$C_{\text{oss}}$		-	62	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	5.3	-	
Total Gate Charge	$Q_g(10\text{V})$	$V_{\text{DD}}=50\text{V}, I_D=8\text{A}, V_{\text{GS}}=10\text{V}$	-	13.5	-	nC
Total Gate Charge	$Q_g(4.5\text{V})$		-	6.5	-	
Gate to Source Charge	$Q_{\text{gs}}$		-	2.8	-	
Gate to Drain (Miller) Charge	$Q_{\text{gd}}$		-	2.0	-	
Turn on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=50\text{V}, I_D=8\text{A}, V_{\text{GS}}=10\text{V}, R_G=10\Omega,$	-	7	-	ns
Rise time	$t_r$		-	4	-	
Turn off Delay Time	$t_{\text{d}(\text{off})}$		-	20	-	
Fall Time	$t_f$		-	4	-	

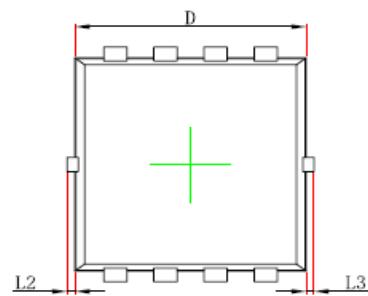
**Reverse Diode Characteristics**

Diode Forward Voltage	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_F=20\text{A}$	-	0.9	1.2	V
Reverse Recovery Time	$t_{\text{rr}}$	$V_R=50\text{V}, I_F=8\text{A}, dI_F/dt=500\text{A}/\mu\text{s}$	-	25	-	ns
Reverse Recovery Charge	$Q_{\text{rr}}$		-	33	-	nC

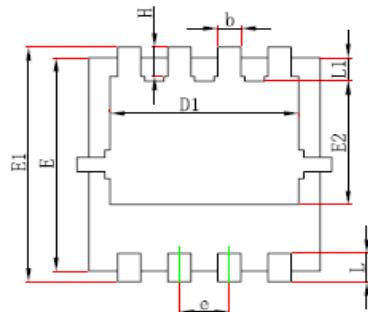
**Fig 1. Typical Output Characteristics**

**Figure 2. On-Resistance vs. Gate-Source Voltage**

**Figure 3. On-Resistance vs. Drain Current and Gate Voltage**

**Figure 4. Normalized On-Resistance vs. Junction Temperature**

**Figure 5. Typical Transfer Characteristics**

**Figure 6. Typical Source-Drain Diode Forward Voltage**


**Figure 7. Typical Gate-Charge vs. Gate-to-Source Voltage**

**Figure 8. Typical Capacitance vs. Drain-to-Source Voltage**

**Figure 9. Maximum Safe Operating Area**

**Figure 10. Maximum Drain Current vs. Case Temperature**

**Figure 11. Normalized Maximum Transient Thermal Impedance, Junction-to-Ambient**


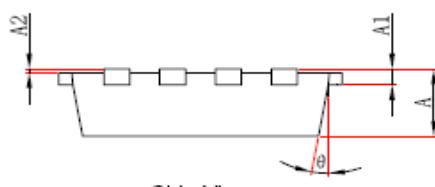


**Package Outline**
**DFN3.3\*3.3\_P, 8 Leads**


Top View



Bottom View



Side View

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.650	0.850	0.026	0.033
A1	0.152 REF.		0.006 REF.	
A2	0~0.05		0~0.002	
D	2.900	3.100	0.114	0.122
D1	2.300	2.600	0.091	0.102
E	2.900	3.100	0.114	0.122
E1	3.150	3.450	0.124	0.136
E2	1.535	1.935	0.060	0.076
b	0.200	0.400	0.008	0.016
e	0.550	0.750	0.022	0.030
L	0.300	0.500	0.012	0.020
L1	0.180	0.480	0.007	0.019
L2	0~0.100		0~0.004	
L3	0~0.100		0~0.004	
H	0.315	0.515	0.012	0.020
$\theta$	9°	13°	9°	13°