

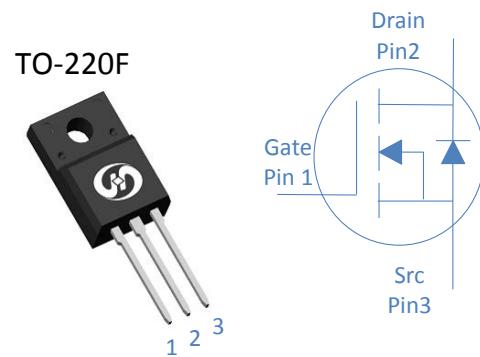
**100V N-Ch Power MOSFET**
**Feature**

- ◊ Optimized for high speed smooth switching
- ◊ Enhanced Body diode dv/dt capability
- ◊ Enhanced Avalanche Ruggedness
- ◊ 100% UIS Tested, 100% R<sub>g</sub> Tested
- ◊ Lead Free

$V_{DS}$	100	V
$R_{DS(on),typ}$	6.4	mΩ
$R_{DS(on),max}$	8.2	mΩ
$I_D$	48	A

**Application**

- ◊ Synchronous Rectification in SMPS
- ◊ Hard Switching and High Speed Circuit
- ◊ Power Tools
- ◊ UPS
- ◊ Motor Control



Part Number	Package	Marking
HGA082N10M	TO-220F	GA082N10M

**Absolute Maximum Ratings at  $T_j=25^\circ\text{C}$  (unless otherwise specified)**

Parameter	Symbol	Conditions	Value	Unit
Continuous Drain Current	$I_D$	$T_C=25^\circ\text{C}$	48	A
		$T_C=100^\circ\text{C}$	34	
Drain to Source Voltage	$V_{DS}$	-	100	V
Gate to Source Voltage	$V_{GS}$	-	$\pm 20$	V
Pulsed Drain Current	$I_{DM}$	-	390	A
Avalanche Energy, Single Pulse	$E_{AS}$	$L=0.5\text{mH}, T_C=25^\circ\text{C}$	400	mJ
Power Dissipation	$P_D$	$T_C=25^\circ\text{C}$	42	W
Operating and Storage Temperature	$T_J, T_{stg}$	-	-55 to 175	°C

**Absolute Maximum Ratings**

Parameter	Symbol	Max	Unit
Thermal Resistance Junction-Case	$R_{thJC}$	3.6	°C/W
Thermal Resistance Junction-Ambient	$R_{thJA}$	65	°C/W

**Electrical Characteristics at  $T_j=25^\circ\text{C}$  (unless otherwise specified)**
**Static Characteristics**

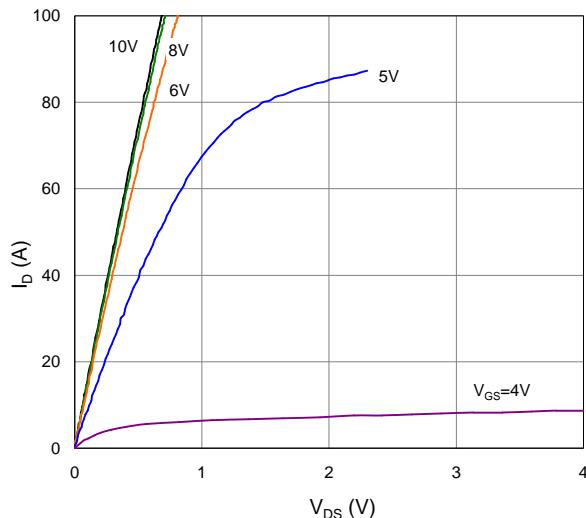
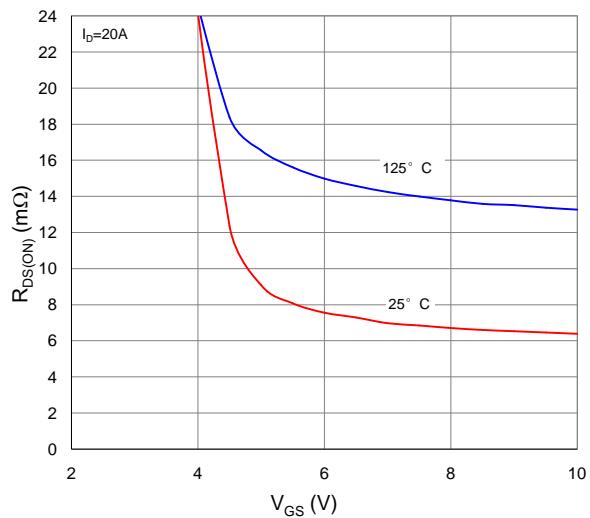
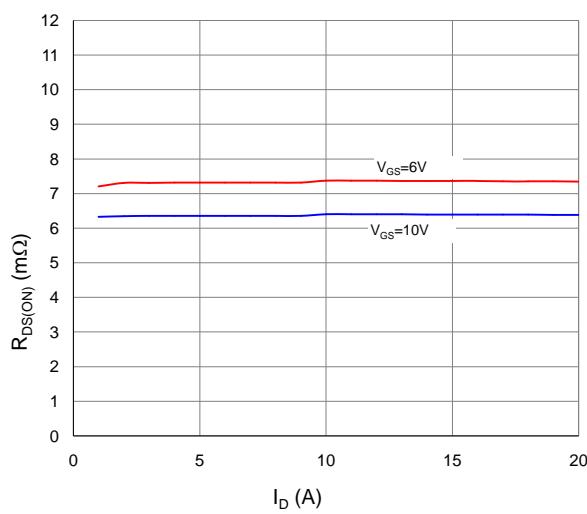
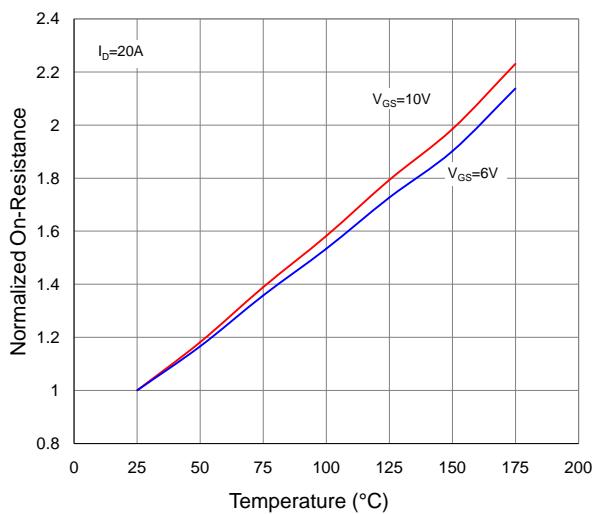
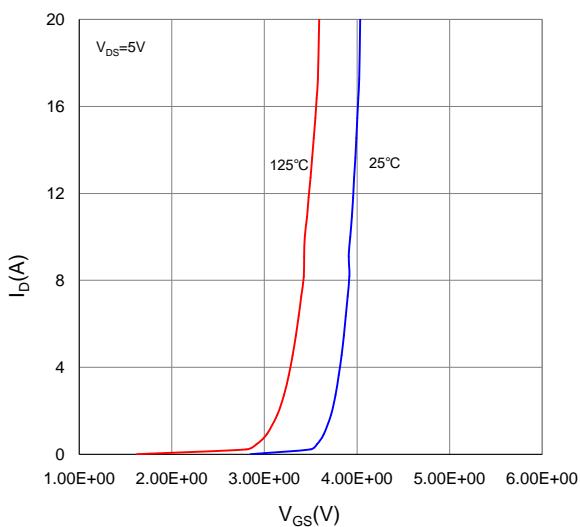
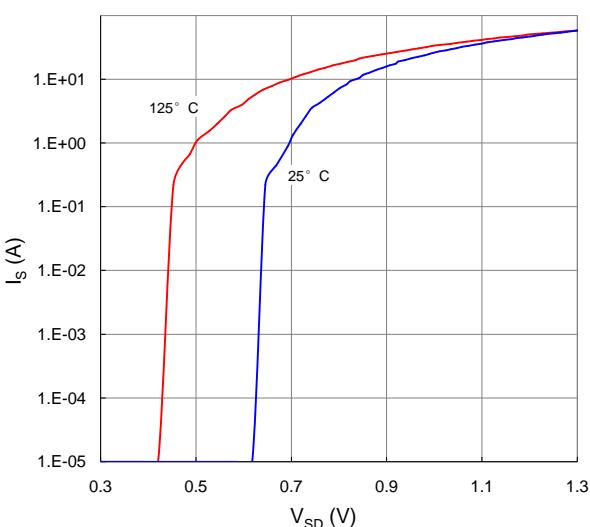
Parameter	Symbol	Conditions	Value			Unit	
			min	typ	max		
Drain to Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	100	-	-	V	
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}, I_D=250\mu\text{A}$	2	2.8	4		
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=100\text{V}, T_j=25^\circ\text{C}$	-	-	1	$\mu\text{A}$	
		$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=100\text{V}, T_j=100^\circ\text{C}$	-	-	100		
Gate to Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm100$	nA	
Drain to Source on Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=10\text{V}, I_D=20\text{A}$	TO-220F	-	6.4	8.2	$\text{m}\Omega$
Transconductance	$g_{\text{fs}}$	$V_{\text{DS}}=5\text{V}, I_D=20\text{A}$		-	75	-	S
Gate Resistance	$R_G$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}} \text{ Open}, f=1\text{MHz}$	-	1.6	-	$\Omega$	

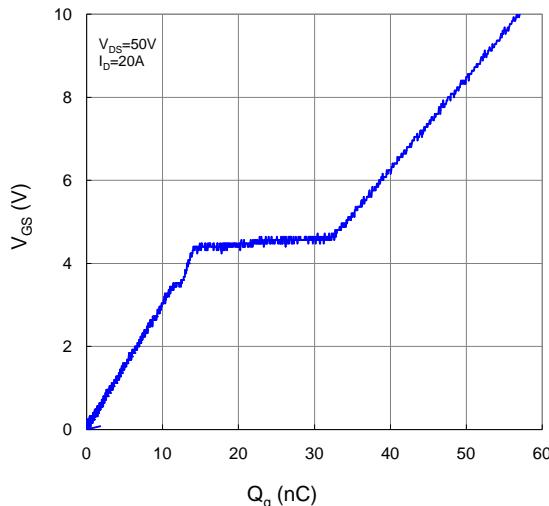
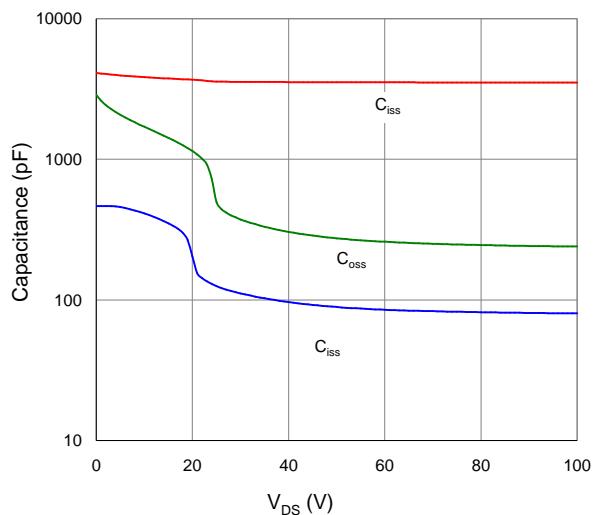
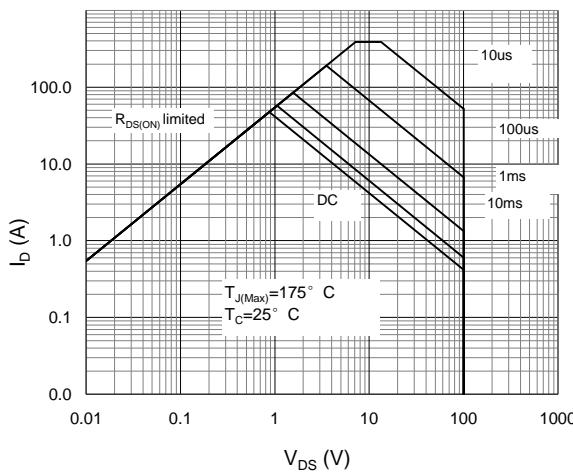
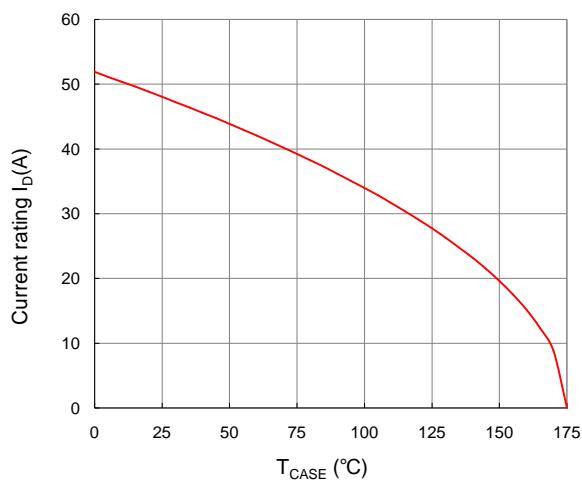
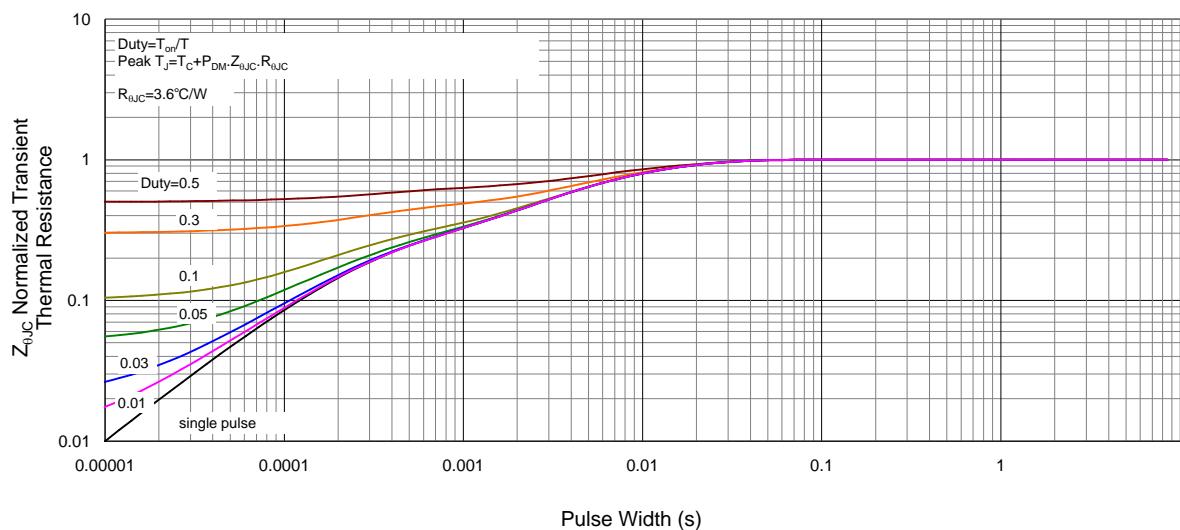
**Dynamic Characteristics**

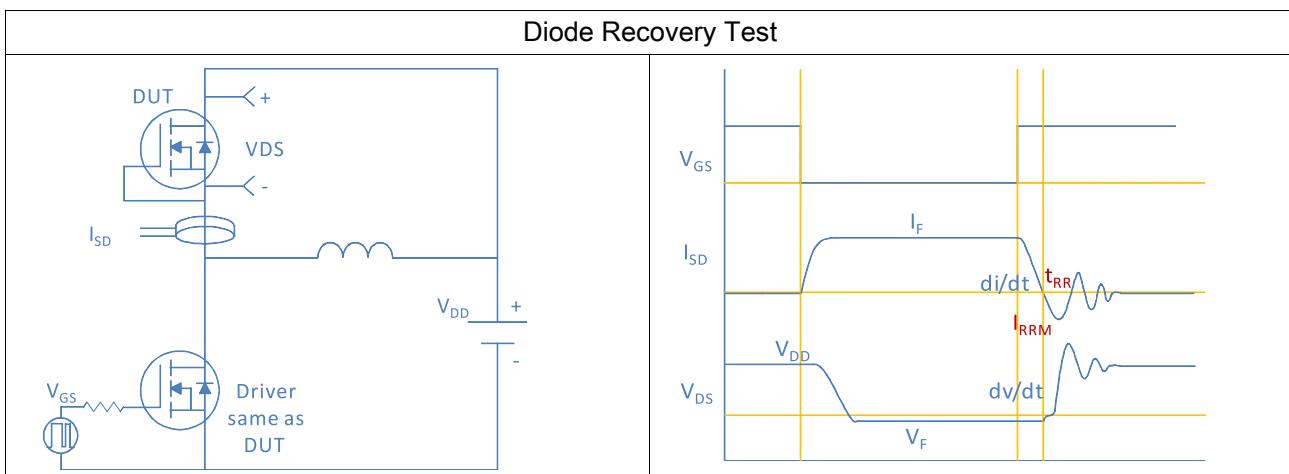
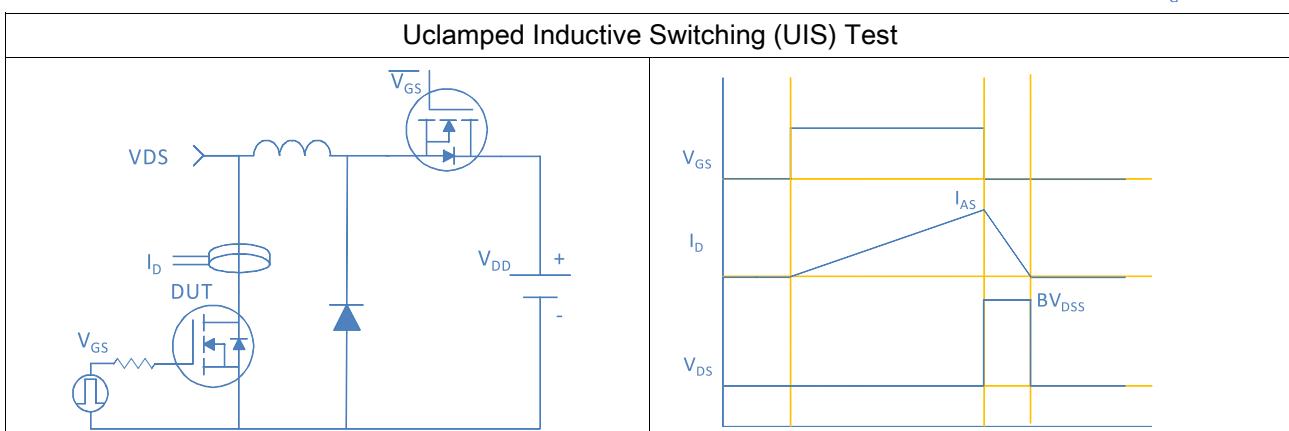
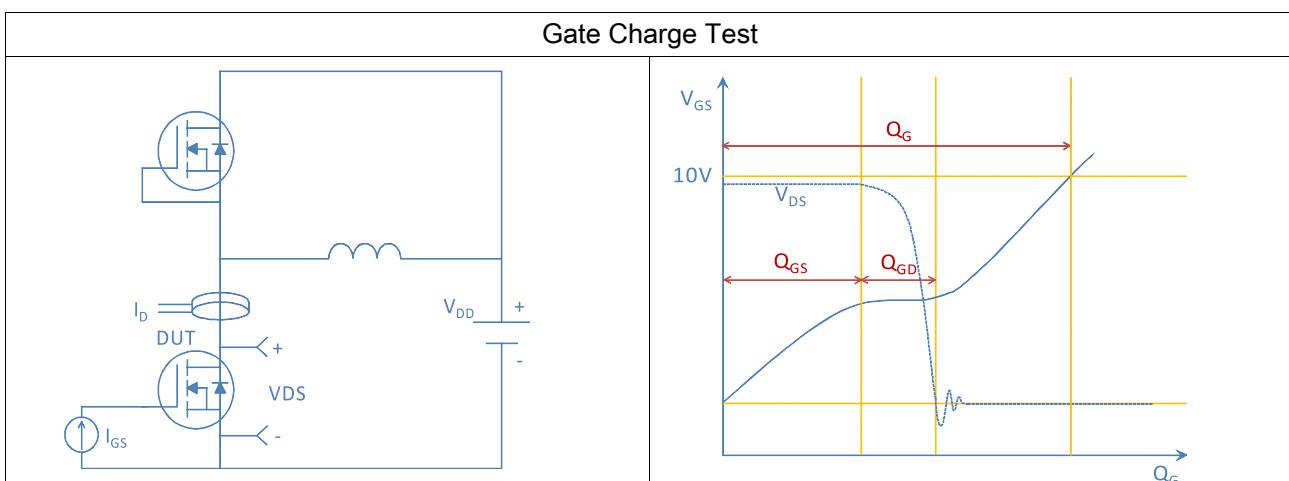
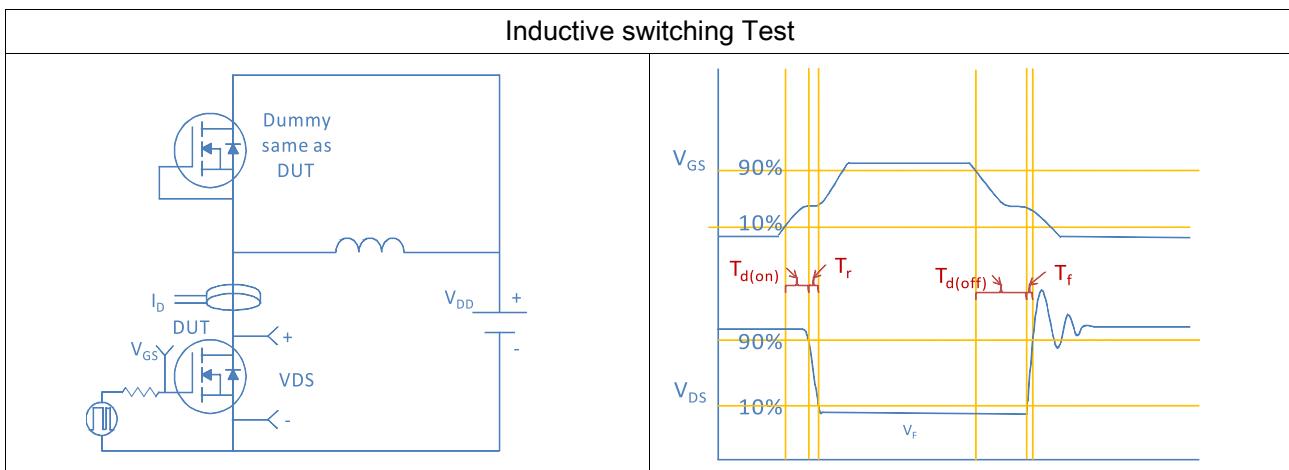
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=50\text{V}, f=1\text{MHz}$	-	3650	-	pF
Output Capacitance	$C_{\text{oss}}$		-	290	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	88	-	
Total Gate Charge	$Q_g$	$V_{\text{DD}}=50\text{V}, I_D=20\text{A}, V_{\text{GS}}=10\text{V}$	-	56	-	nC
Gate to Source Charge	$Q_{\text{gs}}$		-	14	-	
Gate to Drain (Miller) Charge	$Q_{\text{gd}}$		-	18	-	
Turn on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=50\text{V}, I_D=20\text{A}, V_{\text{GS}}=10\text{V}$ , $R_G=10\Omega$ ,	-	17	-	ns
Rise time	$t_r$		-	40	-	
Turn off Delay Time	$t_{\text{d}(\text{off})}$		-	57	-	
Fall Time	$t_f$		-	37	-	

**Reverse Diode Characteristics**

Diode Forward Voltage	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_F=20\text{A}$	-	0.9	1.2	V
Reverse Recovery Time	$t_{\text{rr}}$	$V_R=50\text{V}, I_F=20\text{A}, dI_F/dt=500\text{A}/\mu\text{s}$	-	50	-	ns
Reverse Recovery Charge	$Q_{\text{rr}}$		-	255	-	nC

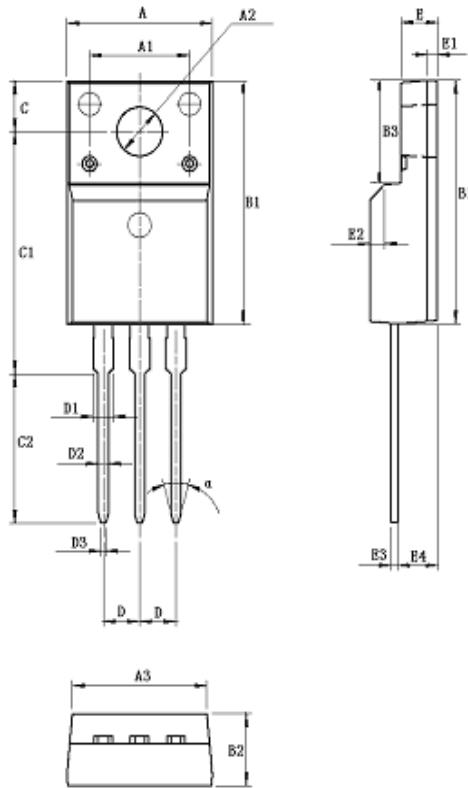
**Fig 1. Typical Output Characteristics**

**Figure 2. On-Resistance vs. Gate-Source Voltage**

**Figure 3. On-Resistance vs. Drain Current and Gate Voltage**

**Figure 4. Normalized On-Resistance vs. Junction Temperature**

**Figure 5. Typical Transfer Characteristics**

**Figure 6. Typical Source-Drain Diode Forward Voltage**


**Figure 7. Typical Gate-Charge vs. Gate-to-Source Voltage**

**Figure 8. Typical Capacitance vs. Drain-to-Source Voltage**

**Figure 9. Maximum Safe Operating Area**

**Figure 10. Maximum Drain Current vs. Case Temperature**

**Figure 11. Normalized Maximum Transient Thermal Impedance, Junction-to-Case**




**Package Outline**
**TO-220F, 3 leads**

Dimentions in mm unless otherwise specified



Symbol	Min	Nom	Max
A	9.96	10.16	10.36
A1		7	
A2	3.08	3.18	3.28
A3	9.26	9.46	9.66
B1	15.67	15.87	16.07
B2	4.50	4.70	4.90
B3	6.48	6.68	6.88
C	3.20	3.30	3.40
C1	15.60	15.80	16.00
C2	9.55	9.75	9.95
D		2.54	
D1			1.47
D2	0.70	0.80	0.90
D3	0.25	0.35	0.45
E	2.34	2.54	2.74
E1		0.70	
E2	1.0x45°		
E3	0.45	0.50	0.60
E4	2.56	2.76	2.96
$\alpha$ (degree)		30°	